

Day : Thursday

Date: 6/1/2006  
Time: 18:38:48**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = PURCELL

First Name = STEPHEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08941684</a>	<a href="#">5873463</a>	150	09/30/1997	TOOL BOX	PURCELL, STEPHEN
<a href="#">09376820</a>	<a href="#">6643726</a>	150	08/18/1999	METHOD OF MANUFACTURE AND APPARATUS OF AN INTEGRATED COMPUTING SYSTEM	PURCELL, STEPHEN
<a href="#">09376830</a>	<a href="#">6745318</a>	150	08/18/1999	METHOD AND APPARATUS OF CONFIGURABLE PROCESSING	PURCELL, STEPHEN
<a href="#">11135755</a>	Not Issued	30	05/23/2005	Time-shifting audio and video programs	PURCELL, STEPHEN
<a href="#">60788690</a>	Not Issued	20	04/03/2006	Flexible interconnect assembly for implantable medical devices	PURCELL, STEPHEN
<a href="#">09099850</a>	<a href="#">6249799</a>	150	06/19/1998	SELECTIVE CARRY BOUNDARY	PURCELL, STEPHEN C
<a href="#">09158748</a>	<a href="#">6460065</a>	150	09/22/1998	CIRUIT AND METHOD FOR PARTIAL PRODUCT PRODUCT BIT SHIFTING	PURCELL, STEPHEN C
<a href="#">09444010</a>	<a href="#">6775414</a>	150	11/19/1999	VARIABLE-LENGTH CODE DECODER	PURCELL, STEPHEN C.
<a href="#">09484549</a>	Not Issued	41	01/18/2000	Method and apparatus for dynamic allocation of processing resources	PURCELL, STEPHEN C.
<a href="#">09972007</a>	<a href="#">6938198</a>	150	10/05/2001	METHOD AND SYSTEM FOR ACCELERATING ETHERNET CHECKSUMS	PURCELL, STEPHEN C.
<a href="#">10865469</a>	Not Issued	20	06/10/2004	Variable-length code decoder	PURCELL, STEPHEN C.
<a href="#">11003768</a>	Not Issued	20	12/02/2004	Transferring execution from one instruction stream to another	PURCELL, STEPHEN C.
<a href="#">11351652</a>	Not Issued	18	02/09/2006	Method and system for implementing low overhead memory access in transpose	PURCELL, STEPHEN C.

				operations	
<u>60283665</u>	Not Issued	159	04/12/2001	Method and apparatus for accelerating ethernet checksums	PURCELL, STEPHEN C.
<u>60304933</u>	Not Issued	159	07/11/2001	Layered crossbar memory switch for scalable graphics systems	PURCELL, STEPHEN C.
<u>60651830</u>	Not Issued	159	02/09/2005	Method and system for overhead memory access in transpose operations	PURCELL, STEPHEN C.
<u>07572198</u>	<u>5253078</u>	150	08/23/1990	SYSTEM FOR COMPRESSION AND DECOMPRESSION OF VIDEO DATA USING DISCRETE COSINE TRANSFORM AND CODING TECHNIQUES	PURCELL, STEPHEN C.
<u>07669818</u>	Not Issued	166	03/15/1991	DECOMPRESSION PROCESSOR FOR VIDEO APPLICATIONS	PURCELL, STEPHEN C.
<u>07891507</u>	<u>5870497</u>	150	05/28/1992	DECODER FOR COMPRESSED VIDEO SIGNALS	PURCELL, STEPHEN C.
<u>08046474</u>	<u>5379356</u>	150	04/13/1993	DECOMPRESSION PROCESSOR FOR VIDEO APPLICATIONS	PURCELL, STEPHEN C.
<u>08105253</u>	<u>5598514</u>	150	08/09/1993	STRUCTURE AND METHOD FOR A MULTISTANDARD VIDEO ENCODER/DECODER	PURCELL, STEPHEN C.
<u>08281334</u>	<u>5625784</u>	150	07/27/1994	VARIABLE LENGTH INSTRUCTIONS PACKED IN A FIXED LENGTH DOUBLE INSTRUCTION	PURCELL, STEPHEN C.
<u>08281377</u>	<u>5623434</u>	150	07/27/1994	STRUCTURE AND METHOD OF USING AN ARITHMETIC AND LOGIC UNIT FOR CARRY PROPAGATION STAGE OF A MULTIPLIER	PURCELL, STEPHEN C.
<u>08285376</u>	<u>5586070</u>	150	08/03/1994	STRUCTURE AND METHOD FOR EMBEDDING TWO SMALL MULTIPLIERS IN A LARGER MULTIPLIER	PURCELL, STEPHEN C.
<u>08285379</u>	<u>5477543</u>	150	08/03/1994	STRUCTURE AND METHOD FOR SHIFTING AND REORDERING A PLURALITY OF DATA BYTES	PURCELL, STEPHEN C.
<u>08296387</u>	<u>5815646</u>	150	10/11/1994	DECOMPRESSION PROCESSOR FOR VIDEO APPLICATIONS	PURCELL, STEPHEN C.

<u>08296777</u>	Not Issued	166	08/26/1994	DECOMPRESSION PROCESSOR FOR VIDEO APPLICATIONS	PURCELL, STEPHEN C.
<u>08296943</u>	5608888	150	08/26/1994	METHOD AND APPARATUS FOR MAPPING DATA OF A 2- DIMENSIONAL SPACE FROM A LINEARLY ADDRESSED MEMORY SYSTEM	PURCELL, STEPHEN C.
<u>08297722</u>	5598483	150	10/24/1994	MPEG VIDEO DECOMPRESSION PROCESSOR	PURCELL, STEPHEN C.
<u>08311670</u>	5568167	150	09/23/1994	SYSTEM FOR PROVIDING ANTIALIASED VIDEO OVERLAYS	PURCELL, STEPHEN C.
<u>08416372</u>	5712799	150	04/04/1995	METHOD AND STRUCTURE FOR PERFORMING MOTION ESTIMATION USING REDUCED PRECISION PIXEL INTENSITY VALUES	PURCELL, STEPHEN C.
<u>08520286</u>	5740340	150	08/28/1995	2-DIMENSIONAL MEMORY ALLOWING ACCESS BOTH AS ROWS OF DATA WORDS AND COLUMNS OF DATA WORDS	PURCELL, STEPHEN C.
<u>08520287</u>	5630033	150	08/28/1995	ADAPTIC THRESHOLD FILTER AND METHOD THEREOF	PURCELL, STEPHEN C.
<u>08520288</u>	Not Issued	161	08/28/1995	STRUCTURE AND METHOD FOR A MULTISTANDARD VIDEO ENCODER/DECODER	PURCELL, STEPHEN C.
<u>08520318</u>	5608656	150	08/28/1995	MOTION VECTOR ENCODING CIRCUIT AND METHOD THEREOF	PURCELL, STEPHEN C.
<u>08520319</u>	6071004	150	08/28/1995	NON-LINEAR DIGITAL FILTERS FOR INTERLACED VIDEO SIGNALS AND METHOD THEREOF	PURCELL, STEPHEN C.
<u>08520353</u>	6122442	150	08/28/1995	STRUCTURE AND METHOD FOR MOTION ESTIMATION OF A DIGITAL IMAGE BY MATCHING DERIVED SCORES	PURCELL, STEPHEN C.
<u>08537495</u>	5664154	150	10/02/1995	M/A FOR OPTIMIZING RETRY TIME UPON CACHE-MISS BY SELECTING A DELAY TIME ACCORDING TO WHETHER THE ADDRESSED LOCATION'S DIRTY BIT INDICATES A	PURCELL, STEPHEN C.

				WRITE-BACK	
<u>08541562</u>	<u>5812437</u>	150	10/10/1995	PROGRAMMABLE LOGIC UNIT FOR ARITHMETIC, LOGIC AND EQUALITY FUNCTIONS	PURCELL, STEPHEN C.
<u>08541563</u>	<u>5751622</u>	150	10/10/1995	STRUCTURE AND METHOD FOR SIGNED MULTIPLICATION USING LARGE MULTIPLIER HAVING TWO EMBEDDED SIGNED MULTIPLIERS	PURCELL, STEPHEN C.
<u>08541565</u>	<u>5864704</u>	150	10/10/1995	MULTIMEDIA PROCESSOR USING VARIABLE LENGTH INSTRUCTIONS WITH OPCODE SPECIFICATION OF SOURCE OPERAND AS RESULT OF PRIOR INSTRUCTION	PURCELL, STEPHEN C.
<u>08577032</u>	<u>5719802</u>	150	12/22/1995	ADDER CIRCUIT INCORPORATING BYTE BOUNDARIES	PURCELL, STEPHEN C.
<u>08730252</u>	<u>5809174</u>	150	10/15/1996	DECOMPRESSION PROCESSOR FOR VIDEO APPLICATIONS	PURCELL, STEPHEN C.
<u>08880357</u>	<u>5910909</u>	150	06/23/1997	NON-LINEAR DIGITAL FILTERS FOR INTERLACED VIDEO SIGNALS AND METHOD THEREOF	PURCELL, STEPHEN C.
<u>09100266</u>	<u>6081823</u>	150	06/19/1998	CIRCUIT AND METHOD FOR WRAP-AROUND SIGN EXTENSION FOR SIGNED NUMBERS	PURCELL, STEPHEN C.
<u>09100385</u>	<u>6286023</u>	150	06/19/1998	PARTITIONED ADDER TREE SUPPORTED BY A MULTIPLEXER CONFIGURATION	PURCELL, STEPHEN C.
<u>09100446</u>	<u>6073156</u>	150	06/19/1998	CIRCUIT AND METHOD FOR WRAP-AROUND SIGN EXTENSION FOR SIGNED NUMBERS USING REPLACEMENT OF MOST SIGNIFICANT BIT	PURCELL, STEPHEN C.
<u>09159271</u>	<u>6393453</u>	150	09/22/1998	CIRCUIT AND METHOD FOR FAST SQUARING	PURCELL, STEPHEN C.
<u>09328844</u>	Not Issued	161	06/09/1999	METHOD AND APPARATUS FOR SHARING CACHE	PURCELL, STEPHEN C.

				MEMORY AMONG MULTIPLE PROCESSORS	
<u>09328971</u>	<u>6449671</u>	150	06/09/1999	METHOD AND APPARATUS FOR BUSING DATA ELEMENTS	PURCELL, STEPHEN C.

[Search and Display More Records.](#)

**Search Another: Inventor**

Last Name	First Name	
<input type="text" value="PURCELL"/>	<input type="text" value="STEPHEN"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Thursday

Date: 6/1/2006  
Time: 18:39:31**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = KIMURA

First Name = SCOTT

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09738733</u>	Not Issued	164	12/15/2000	METHOD AND APPARATUS FOR IMPROVED DATA BUS EFFICIENCY	KIMURA, SCOTT
<u>09925155</u>	Not Issued	95	08/08/2001	METHOD AND APPARATUS FOR GENERATING RANDOM NUMBERS FOR USE IN A FIELD PROGRAMMABLE GATE ARRAY	KIMURA, SCOTT
<u>09925156</u>	<u>6961803</u>	150	08/08/2001	SLICED CROSSBAR ARCHITECTURE WITH NO INTER-SLICE COMMUNICATION	KIMURA, SCOTT
<u>09925158</u>	Not Issued	161	08/08/2001	Memory bus arbitration using memory bank readiness	KIMURA, SCOTT
<u>09925160</u>	Not Issued	71	08/08/2001	One-way transaction tagging in a switch crossbar	KIMURA, SCOTT
<u>09925161</u>	Not Issued	80	08/08/2001	Method and apparatus for accumulating floating point values	KIMURA, SCOTT
<u>09927306</u>	<u>6970454</u>	150	08/09/2001	SLICED CROSSBAR ARCHITECTURE WITH INTER-SLICE COMMUNICATION	KIMURA, SCOTT
<u>10711115</u>	Not Issued	95	08/25/2004	METHOD AND APPARATUS FOR GENERATING RANDOM NUMBERS FOR USE IN A FIELD PROGRAMMABLE GATE ARRAY	KIMURA, SCOTT
<u>10759376</u>	Not Issued	93	01/15/2004	MEMORY BUS ARBITRATION USING MEMORY BANK READINESS	KIMURA, SCOTT
<u>11136080</u>	Not Issued	30	05/23/2005	Sliced crossbar architecture with inter-slice communication	KIMURA, SCOTT
<u>11218963</u>	Not	40	09/01/2005	Sliced crossbar architecture with	KIMURA, SCOTT

	Issued			no inter-slice communication	
<u>11304396</u>	Not Issued	20	12/15/2005	System for performing finite input response (FIR) filtering in motion estimation	KIMURA, SCOTT A.
<u>08127219</u>	<u>5442747</u>	150	09/27/1993	FLEXIBLE MULTIPORT MULTIFORMAT BURST BUFFER	KIMURA, SCOTT A.
<u>08136621</u>	<u>5463422</u>	150	10/13/1993	DATA PROCESSING TECHNIQUE FOR LIMITING THE BANDWIDTH OF DATA TO BE STORED IN A BUFFER	KIMURA, SCOTT A.
<u>08142623</u>	<u>5568165</u>	150	10/22/1993	VIDEO PROCESSING TECHNIQUE USING MULTI-BUFFER VIDEO MEMORY	KIMURA, SCOTT A.
<u>08206491</u>	<u>5469223</u>	150	03/04/1994	SHARED LINE BUFFER ARCHITECTURE FOR A VIDEO PROCESSING CIRCUIT	KIMURA, SCOTT A.
<u>08354187</u>	<u>5696527</u>	150	12/12/1994	MULTIMEDIA OVERLAY SYSTEM FOR GRAPHICS AND VIDEO	KIMURA, SCOTT A.
<u>08354188</u>	<u>5621428</u>	150	12/12/1994	AUTOMATIC ALIGNMENT OF VIDEO WINDOW ON A MULTIMEDIA SCREEN	KIMURA, SCOTT A.
<u>08354190</u>	<u>5644333</u>	150	12/12/1994	COLOR KEY DETECTION SCHEME FOR MULTIMEDIA SYSTEMS	KIMURA, SCOTT A.
<u>08706752</u>	<u>5767863</u>	150	09/03/1996	VIDEO PROCESSING TECHNIQUE USING MULTI-BUFFER VIDEO MEMORY	KIMURA, SCOTT A.

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="KIMURA"/>	<input type="text" value="SCOTT"/>	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)